PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Fabrizio Rovati et al.

Filed

: March 27, 2001

For

COPROCESSOR CIRCUIT ARCHITECTURE, FOR INSTANCE

FOR DIGITAL ENCODING APPLICATIONS

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Box Patent Application Commissioner for Patents Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

Fabrizio Rovati et al.

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European Search Report, Form PTO-1449, Cited References (5)

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